

01/26/01

ISSUE CLASSIFICATION	
Class	Subclass

PATENT NUMBER

U.S. REISSUE Patent Application

12W O.I.P.F. SCANNED <u>11/15</u> Q.A. <u>4/3/01</u>	O.G. PUBLICATION DATE	REISSUE PATENT DATE
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APPLICATION NO. 09/771229	CONT/PRIOR D F	CLASS 375	SUBCLASS 354	ART UNIT 2637	EXAMINER Tse
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Takanori Iwamatsu
Hiroyuki Kiyonagi

Clock phase detecting circuit and clock regenerating circuit each arranged in receiving unit of multiplex radio equipment

PTO-2040
12/89

SURRENDER OF ORIGINAL PATENT _____ (Exr. Initials) **ORIGINAL PATENT NUMBER** _____

ISSUING CLASSIFICATION												
ORIGINAL					CROSS REFERENCE(S)							
CLASS		SUBCLASS			CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)						
INTERNATIONAL CLASSIFICATION												

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	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
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